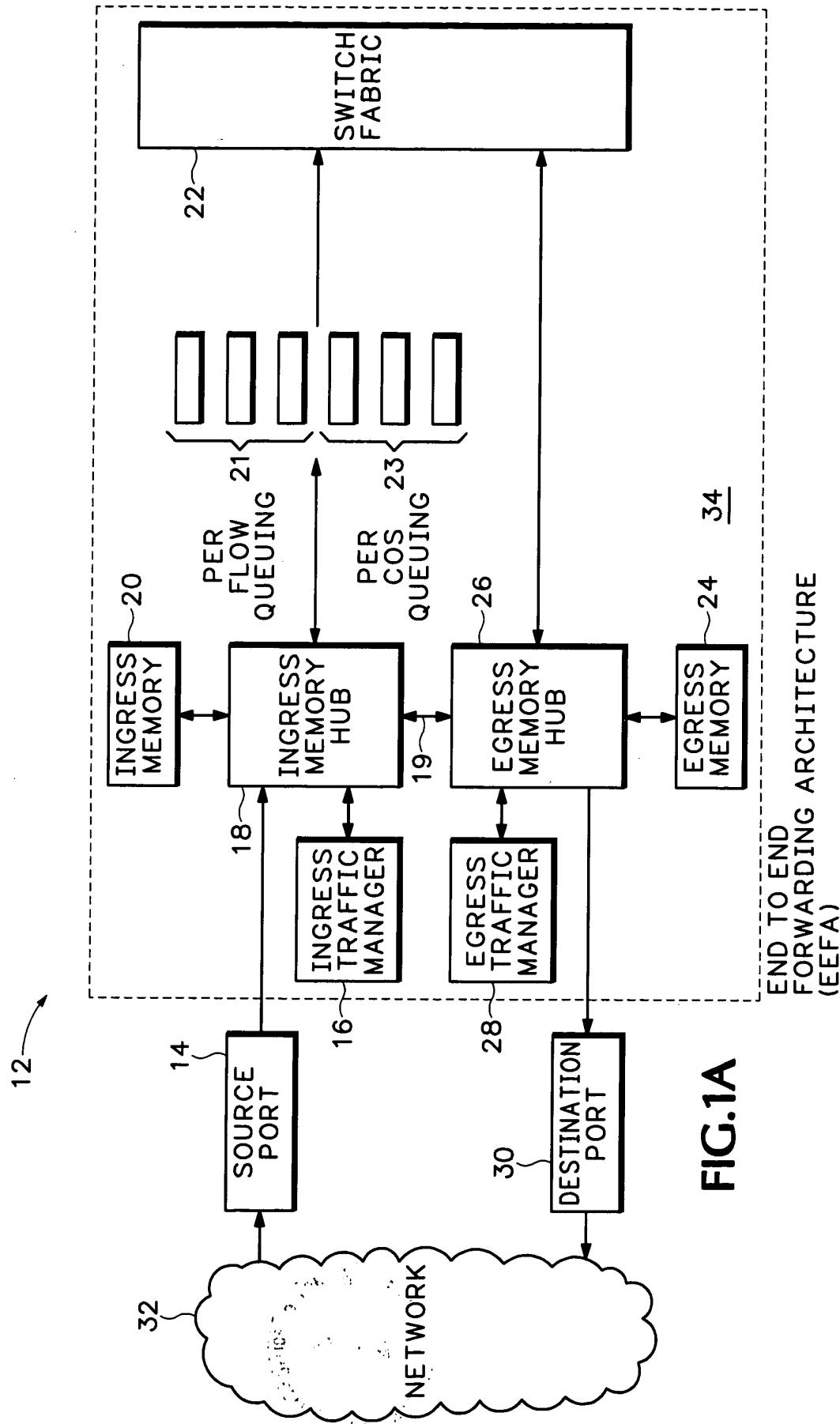


TO DISO SECURE



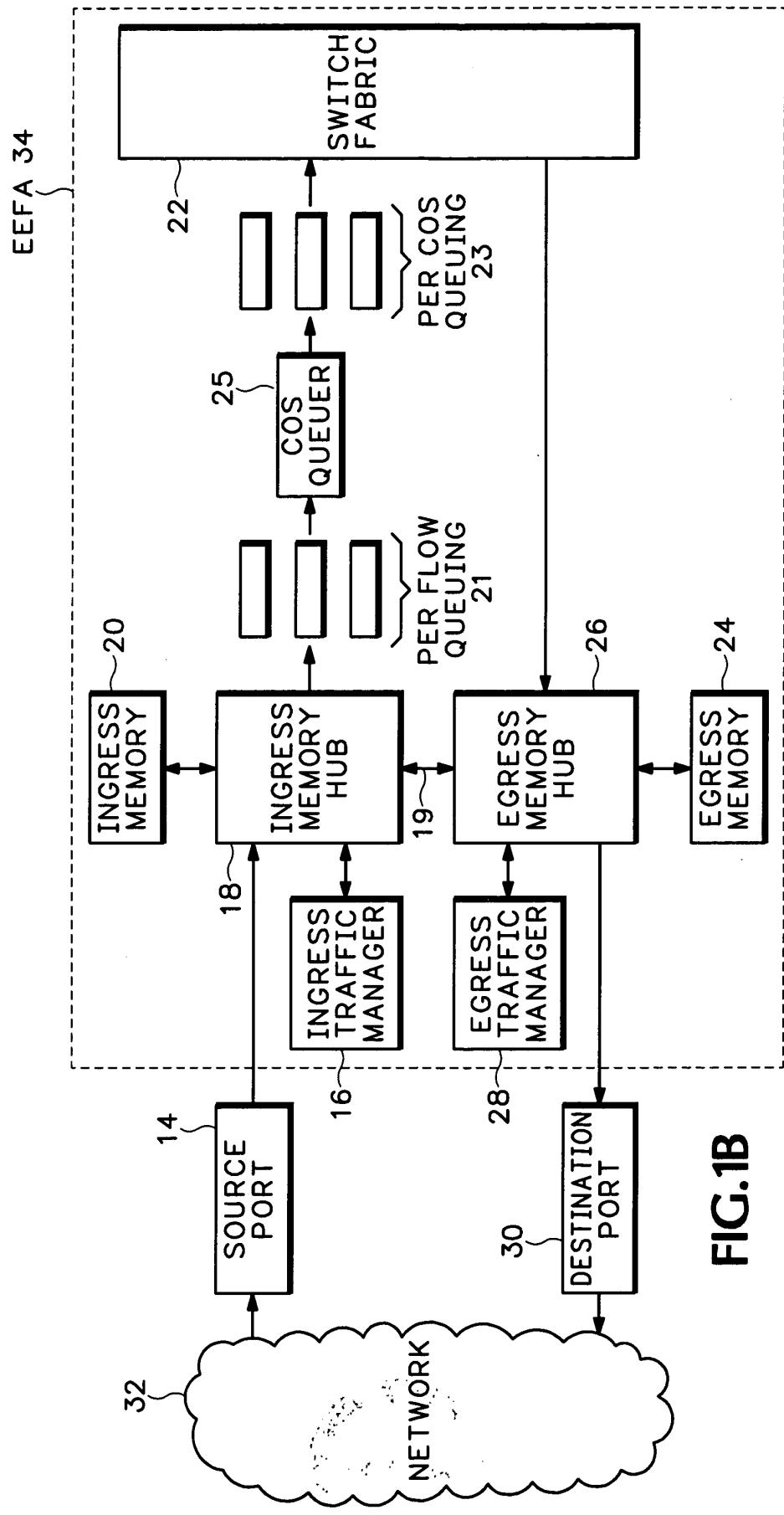


FIG.1B

FIGURE 2 FIGURE 2

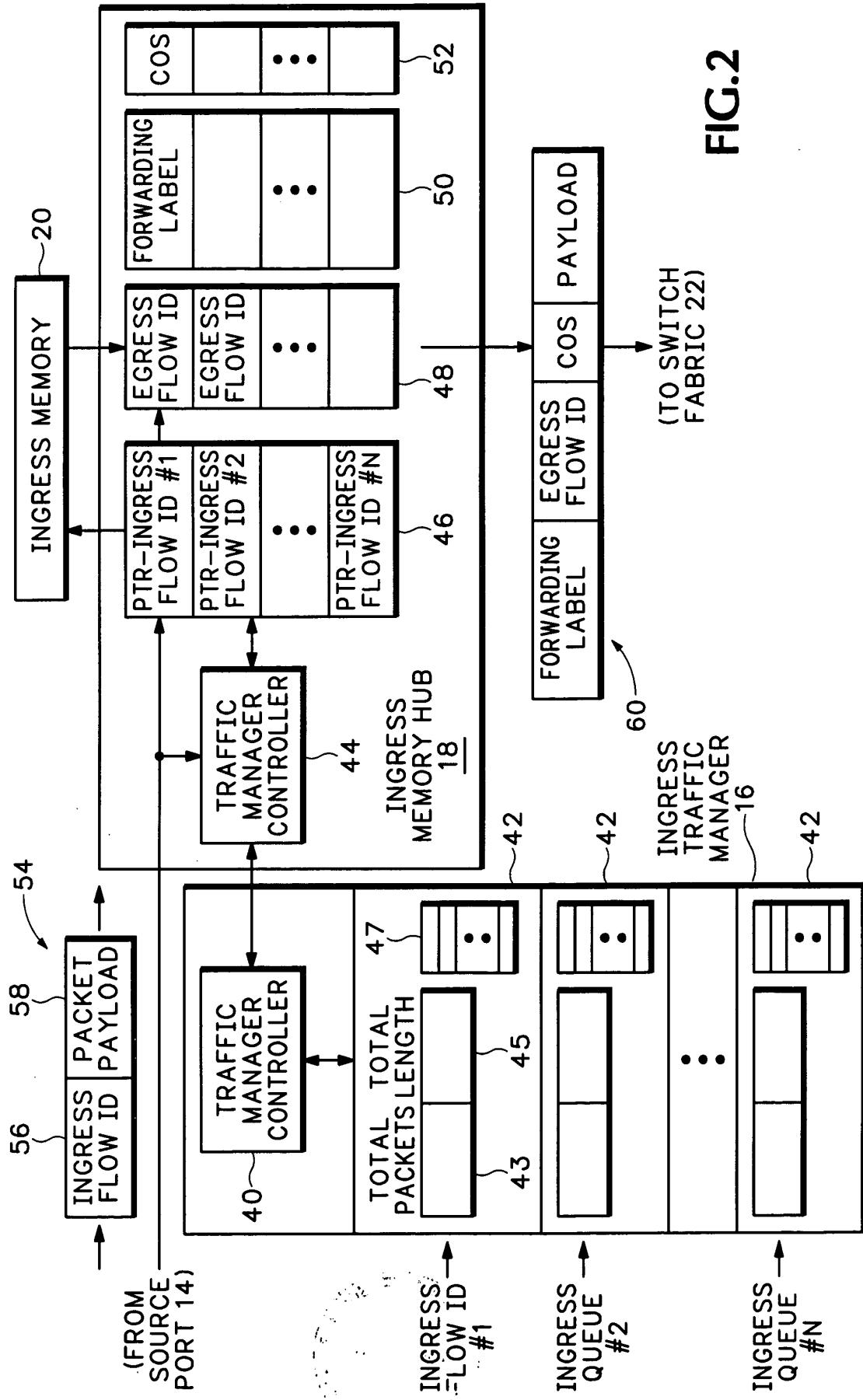


FIG.2

© 2008 IEEE. All rights reserved.

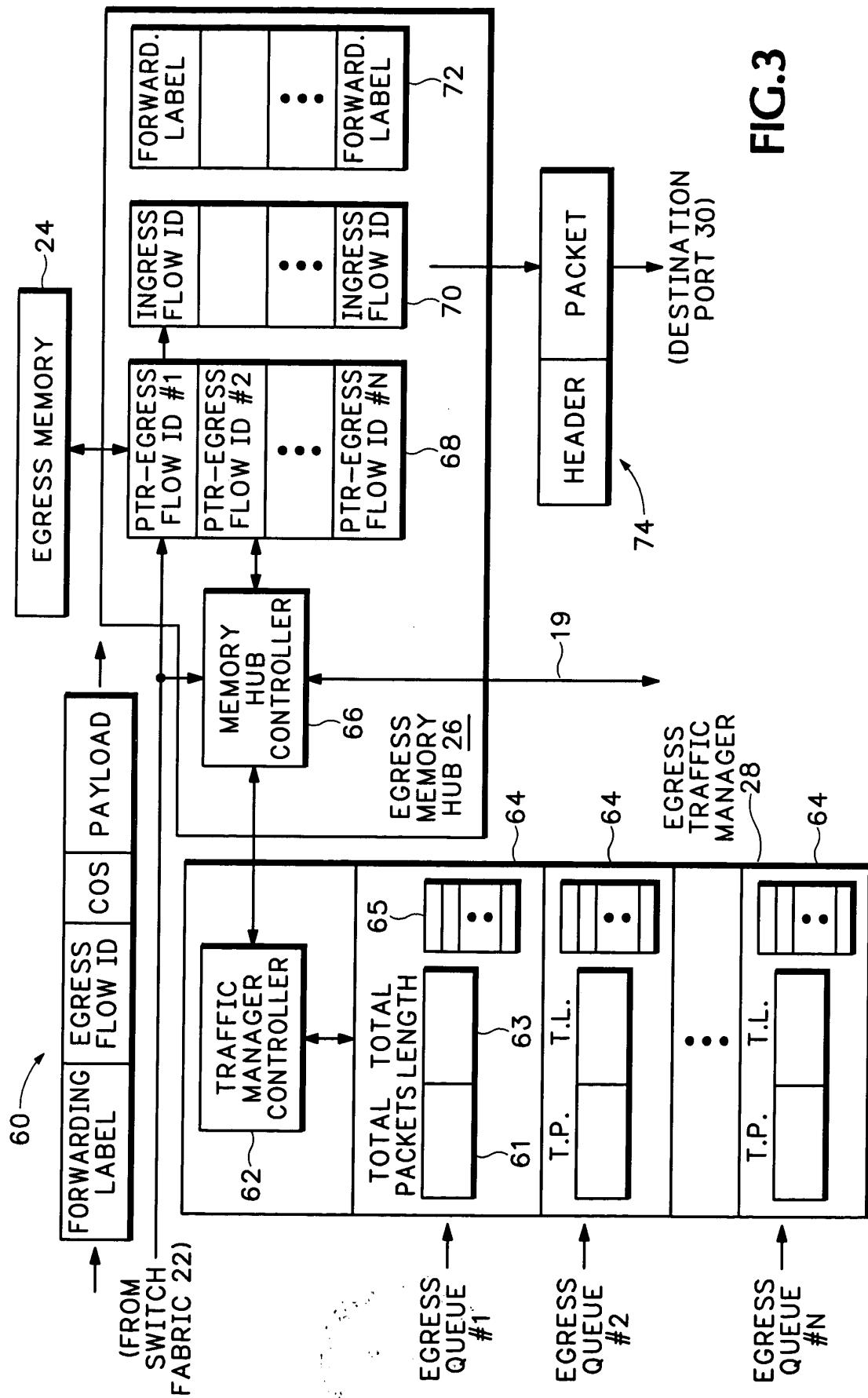
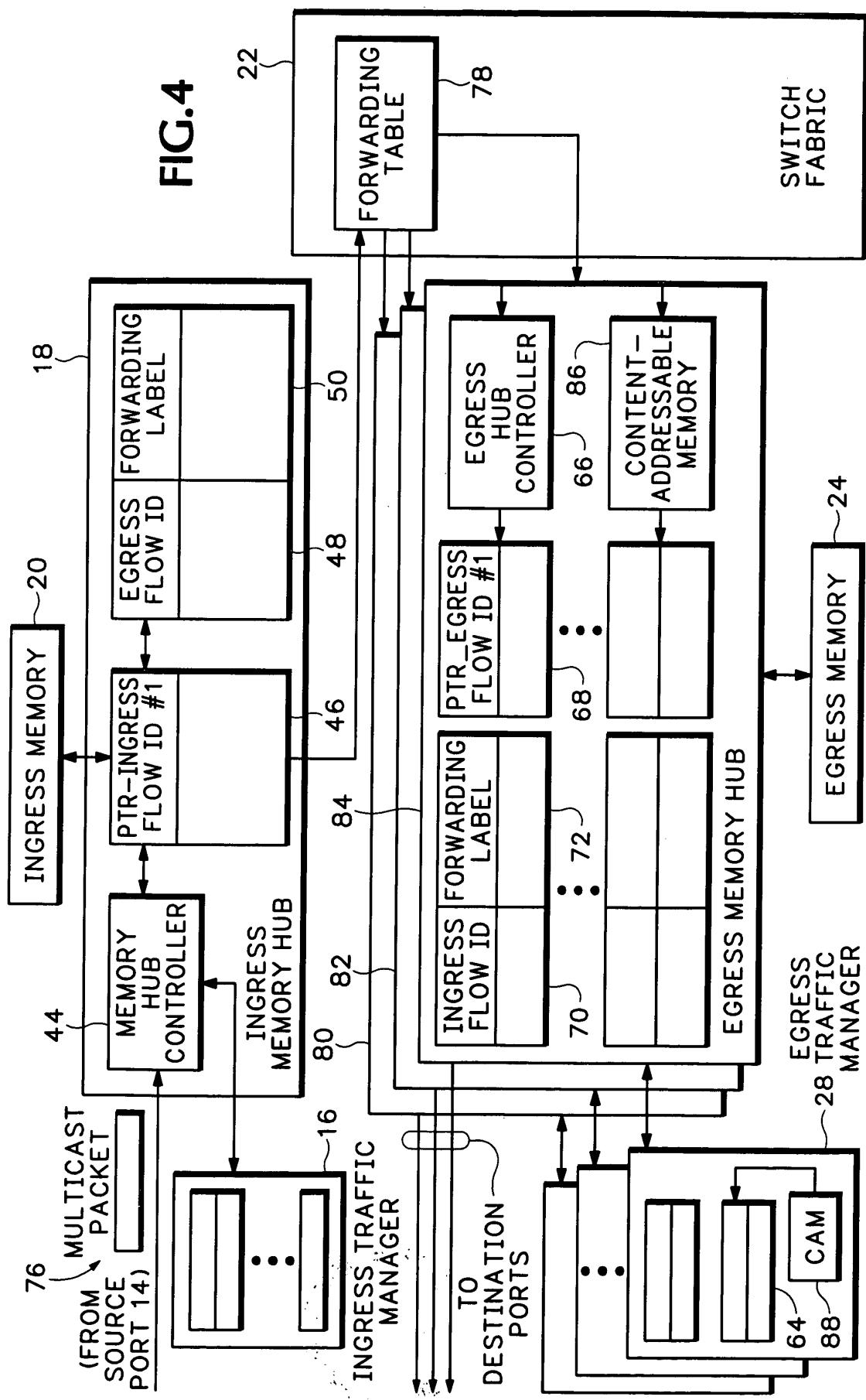


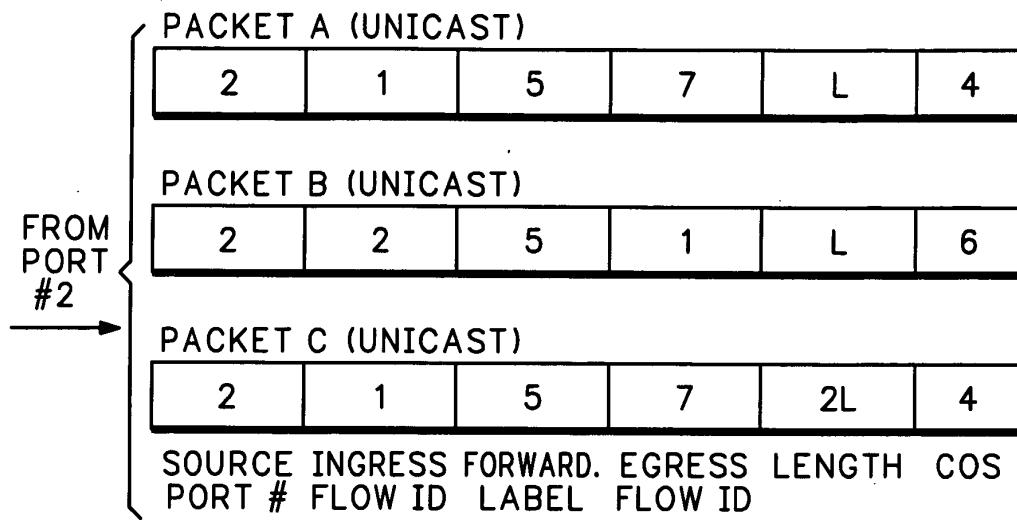
FIG.3

1.0 2'050 "C2E30 J'250

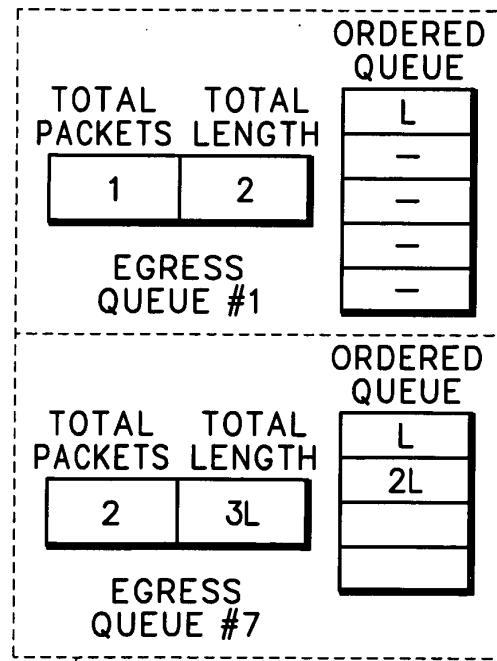
FIG.4



000000000000000000000000



TO FIG.5B

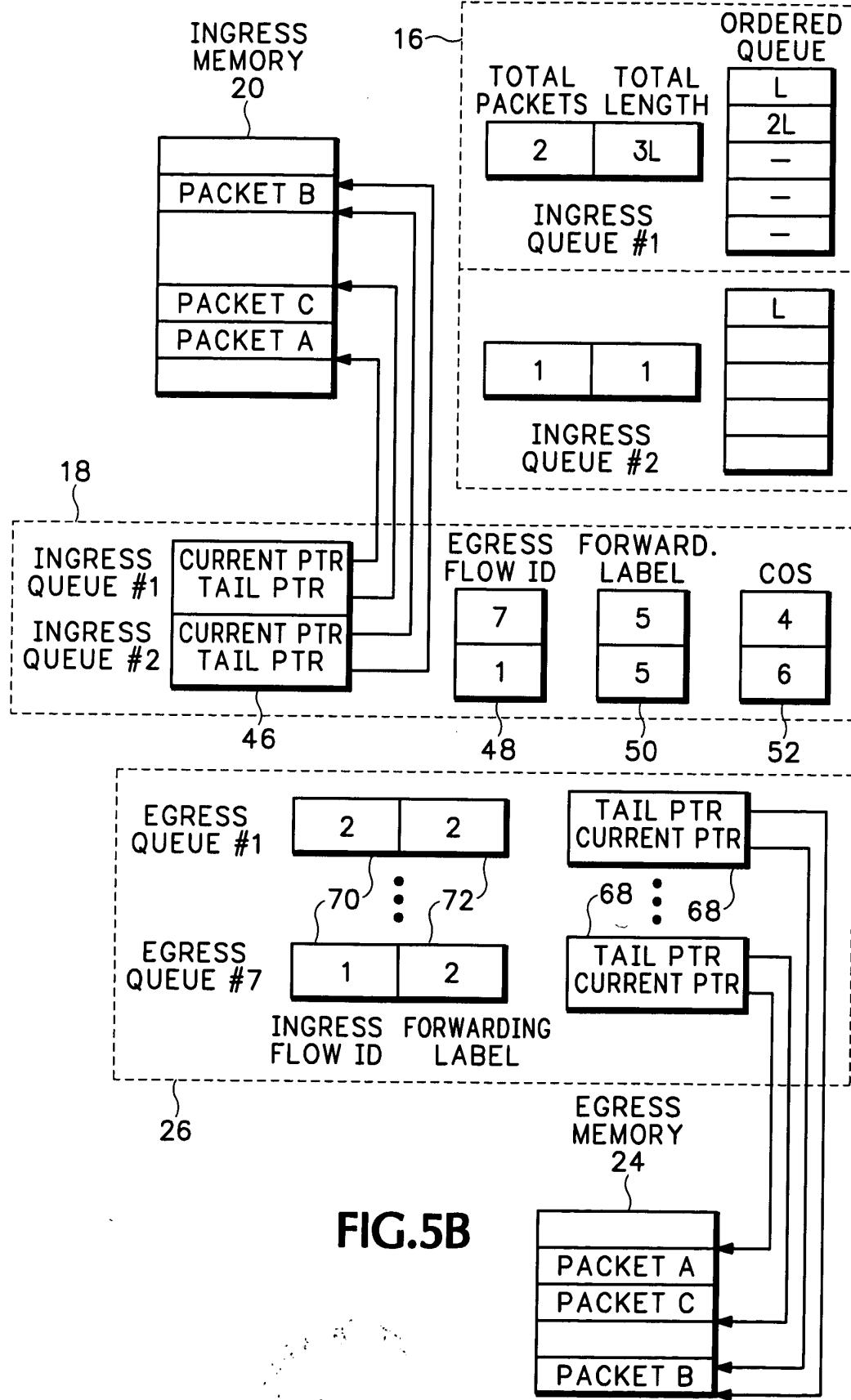


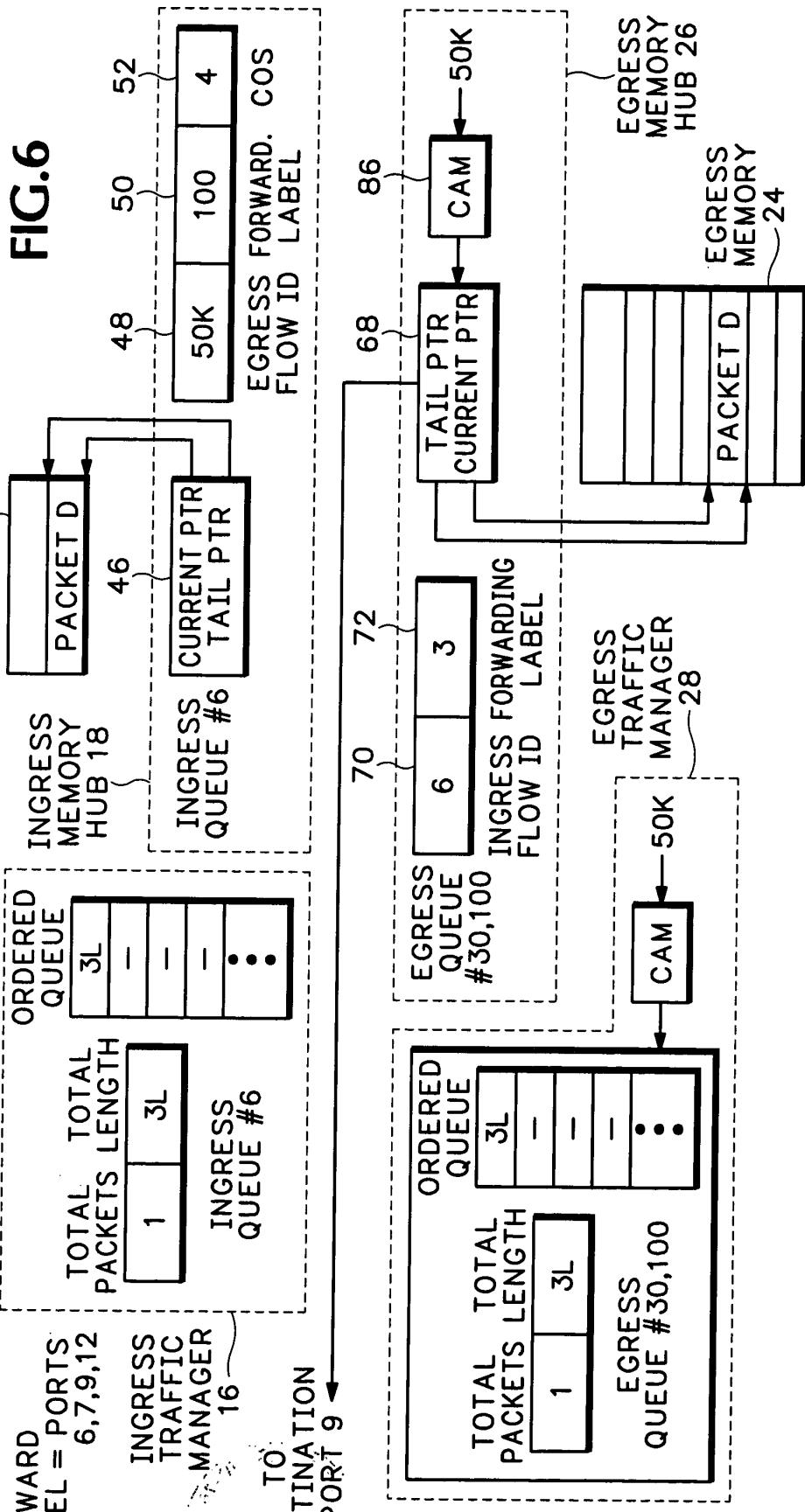
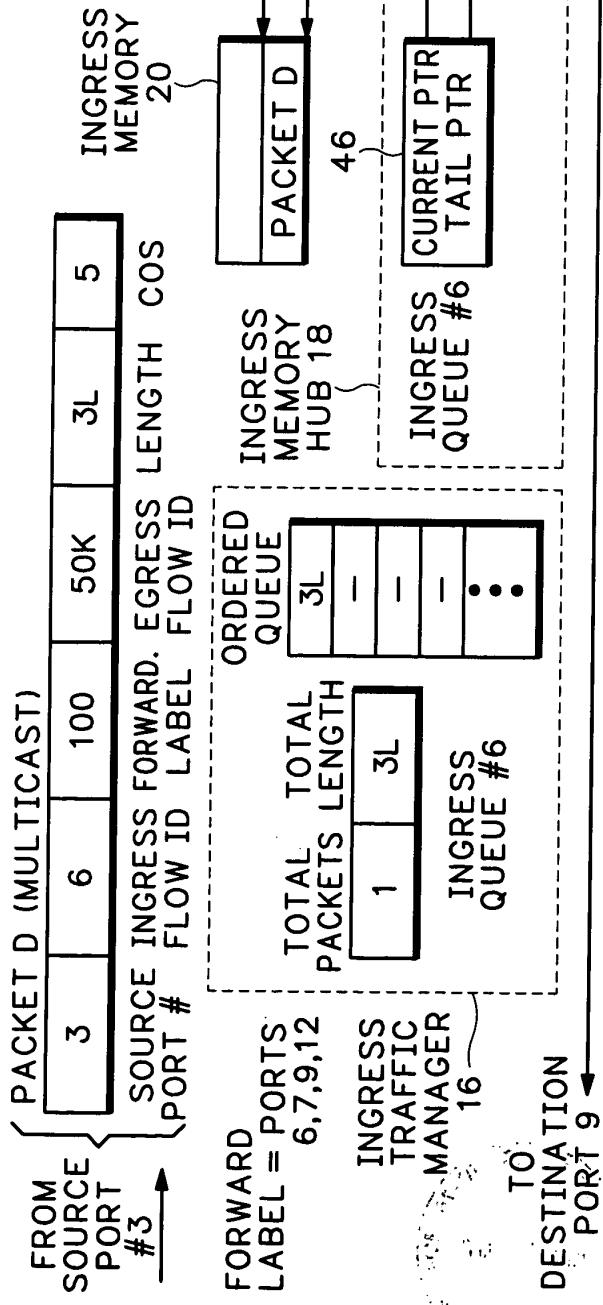
28

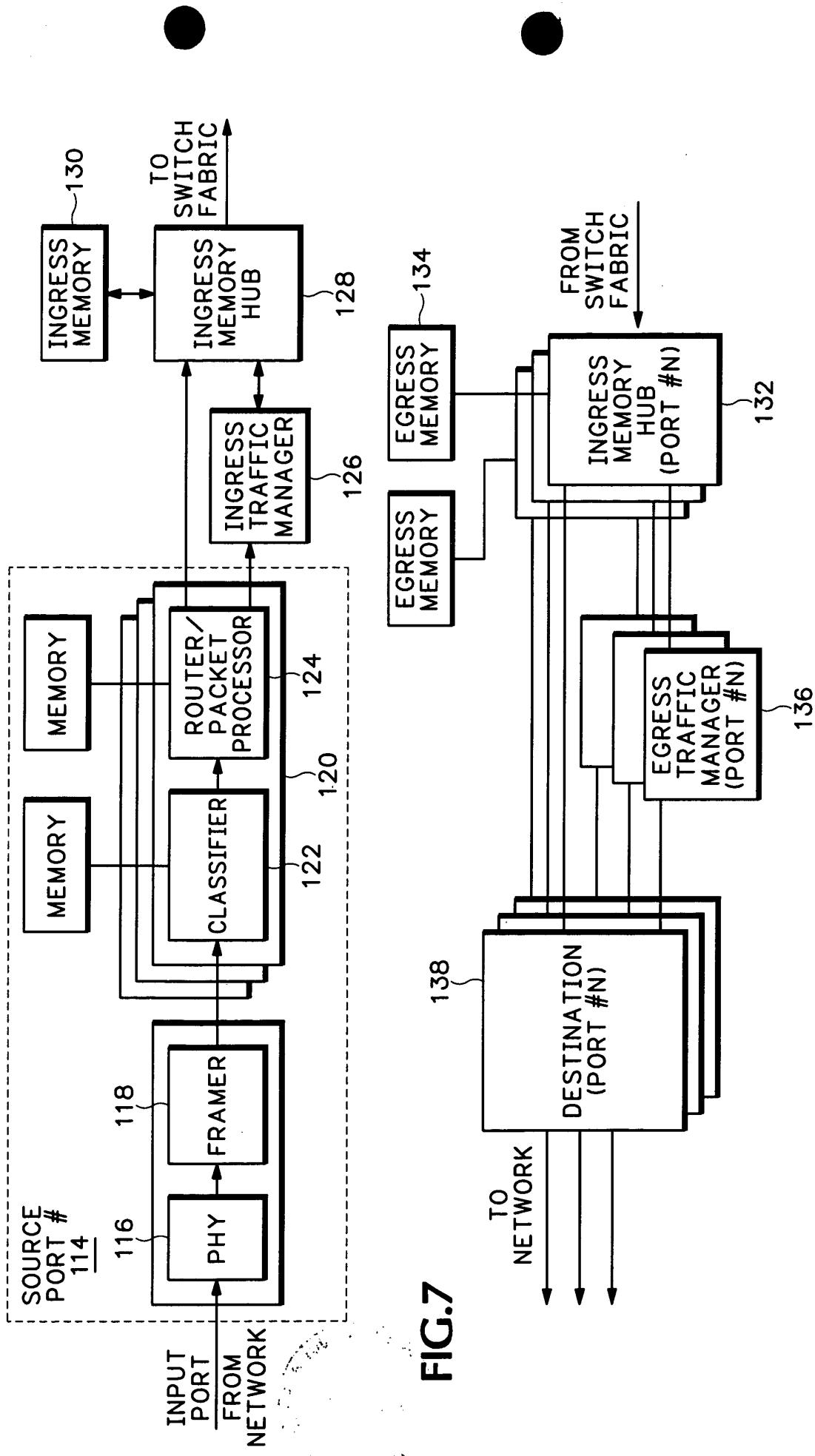
FIG.5A

卷之三

FROM FIG.5A







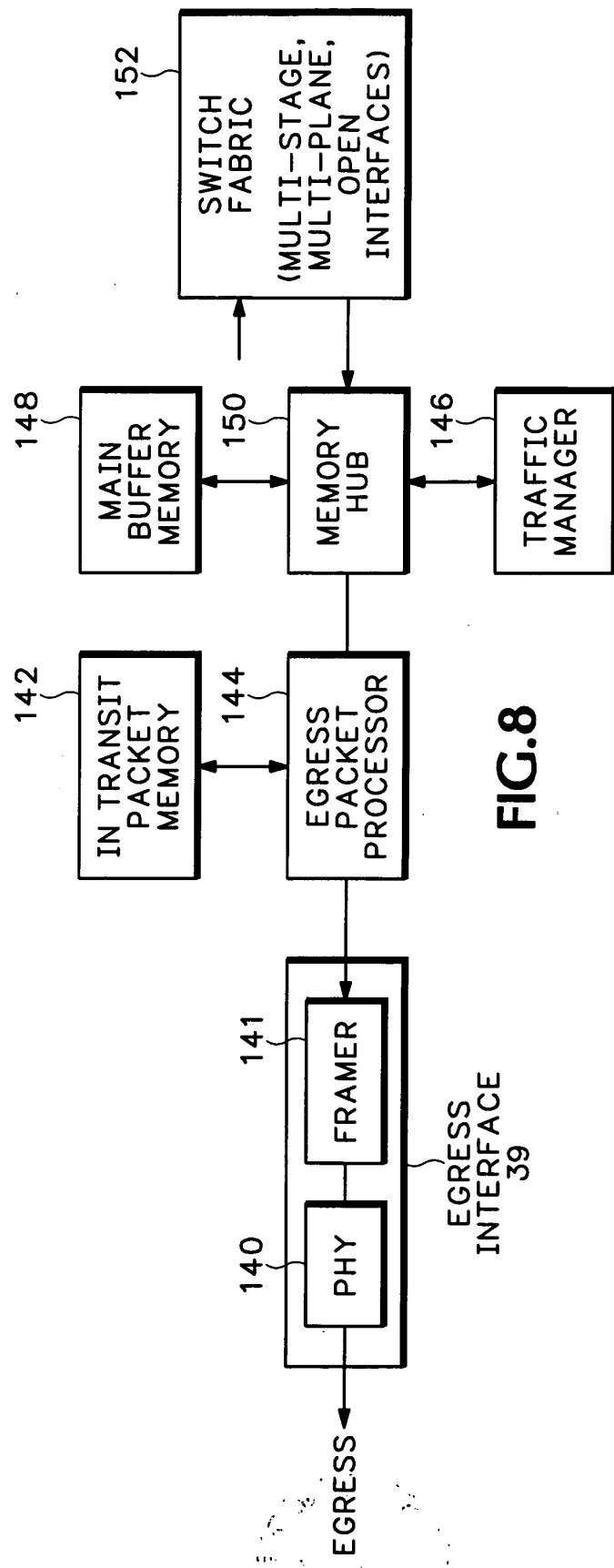
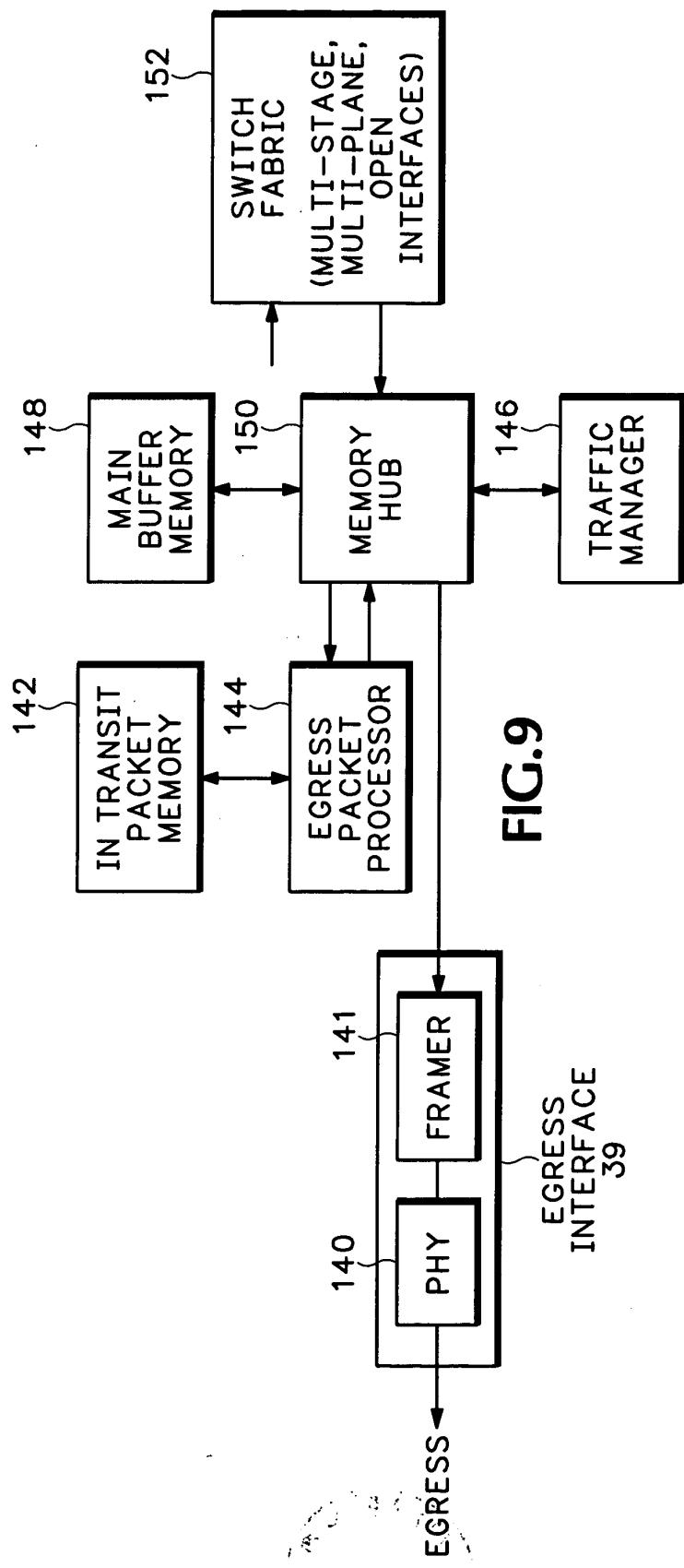


FIG.8

EGRESS
INTERFACE
39

EGRESS

39



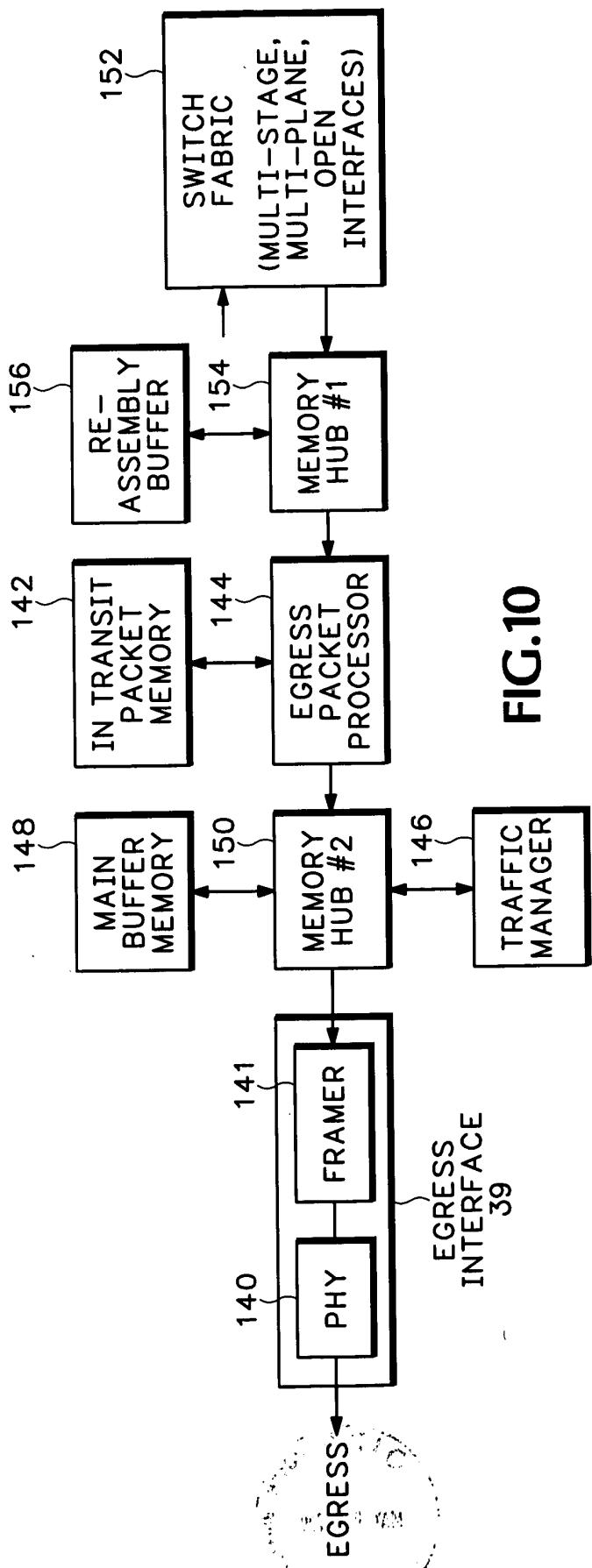


FIG.10